



# SANKETIKA VIDYA PARISHAD ENGINEERING COLLEGE

(Approved by AICTE, New Delhi & Affiliated to Andhra University, Visakhapatnam)

P M Palem, VISAKHAPATNAM – 530 041

svpec@svpec.edu.in

w:www.svpec.edu.in

P: (0891) 2781375, Ex -106

## ECE-A II YEAR TIME TABLE

Branch: ECE		II/IV B.E Semester: I			Room. No: F1		w. e. f:1/7/2017		
DAY	9:30-10:20	10:20- 11:10	11:10-12:00	12:00-1:00	1:00PM TO 1:40PM  LUNCH	1:40-2:30	2:30-3:20	3:20-4:10	
MON	M-IV	DS	STLD	NTA		EDC	EM	LIB	
TUE	EM	EDC	M-IV			NTA	DS		
WED	NTA		M-IV	EM		EDC/NT LAB			
THU	EDC/NT LAB			DS		EDC	STLD		
FRI	STLD	EM		EDC		DS	NTA	M-IV	
SAT	EDC		M-IV	STLD		NTA	EM	DS	

CLASS IN-CHARGE: Mr. K. V. R. Rajesh

M-IV: Mathematics-IV : Ms.R.Madhuri  
DS: Data structures : Mr.Venkateswararao  
STLD: Switching theory and logic : Mr.T.Ravibabu  
NTA: Network theory and analysis : Mr. P.Murari  
EDC: Electronic devices and circuits : Mr. K. V. R. Rajesh  
EM: Electrical machines : Mrs.M. Shoba  
EDC lab: Electronic devices and circuits :Mr. K. V. R. Rajesh  
NT Lab: Network theory lab : Mr. P.Murari

CLASS IN-CHARGE

HOD-ECE



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## ECE-B II YEAR TIME TABLE

Branch: ECE		II/IV B.E Semester: I			Room. No: F2		w. e. f:1/7/2017		
DAY	9:30-10:20	10:20- 11:10	11:10-12:00	12:00-1:00	1:00PM TO 1:40PM  LUNCH	1:40-2:30	2:30-3:20	3:20-4:10	
MON	M-IV	DS	STLD	NTA		EDC	EM	LIB	
TUE	EM	EDC	STLD			NTA	DS		
WED	NTA		M-IV	EM		EDC/NT LAB			
THU	EDC/NT LAB			DS		EDC	M-IV		
FRI	EDC	EM		STLD		DS	NTA	M-IV	
SAT	EDC		M-IV	STLD		NTA	EM	DS	

CLASS IN-CHARGE: Mr. S. S. Kiran

M-IV: Mathematics-IV : Mr. Ch.Venu  
DS: Data structures : Mr. A.Asha Kiran  
STLD:Switching theory and logic : Ms. B. Demullamma  
NTA: Network theory and analysis : Mr. T.Ramesh babu  
EDC: Electronic devices and circuits : Mr. A. Satyanarayana  
EM: Electrical machines :Mr. Ch. Krishna  
EDC lab: Electronic devices and circuits :Mrs. T Satyavani  
NT Lab: Network theory lab : Mr. G.Vasu

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## ECE III/IV YEAR TIME TABLE

Branch: ECE		III/IV B.E Semester: I			Room. No: S1	w. e. f: 1/7/2017		
DAY	9:30-10:20	10:20- 11:10	11:10-12:00	12:00-1:00		1:40-2:30	2:30-3:20	3:20-4:10
MON	COMMS	CS	CRT	AWP		CAO	LICA	DSP
TUE	DSP	MOOCS	CS	LICA		LIC/PDC LAB		
WED	COMMS LAB			CAO		MOOCS	COMMS	LICA
THU	LICA	COMMS	DSP	CS		CAO	AWP	
FRI	AWP	COMMS	CS	DSP		MOOCS	SOFTSKILLS	
SAT	LICA	AWP	CAO			COMMS	DSP	CS

CLASS IN-CHARGE: Ms. Kanaka Kumari

LICA	: Linear IC and applications	: Mr. J Sudheer
COMMS	: Communication systems	: Ms. Kanaka Kumari
CONTS	: Control systems	: Mr.V. Kamaraju
AWP	: Antenna and wave propagation	: Mr. S.V Anand
CAO	: Computer architecture and organization	: Ms.P.Kavya
DSP	: Digital signal processing	: Ms. M. Tejaswi
COMMS LAB	: Communication systems lab	: Ms. Kanaka Kumari
LIC/PDC LAB	: Linear IC/Pulse digital circuits lab	: Mr. J Sudheer
SS	: Softskills	: Mrs. Kasulamma

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## ECE IV/IV YEAR TIME TABLE

Branch: ECE		IV/IV B.E Semester: I			Room. No: S2		w. e. f:01/7/2017		
DAY	9:30-10:20	10:20- 11:10	11:10-12:00	12:00-1:00	1:00PM	1:40-2:30	2:30-3:20	3:20-4:10	
MON	VLSI	DSP	MW	TV&SAT	TO	ITC		LIB	
TUE	TV&SAT	CRT	VLSI	MW		1:40PM	ITC	SOFTSKILLS	
WED	MW	TV&SAT	DSP		LUNCH		DC LAB		
THU	DSP	MW		ITC		VLSI		TV&SAT	
FRI	DSP	VLSI	ITC	TV&SAT	DSP LAB				
SAT	ITC	DSP	MW	VLSI	TV&SAT	SEMINAR			

CLASS IN-CHARGE: Mr. Mr.ManasRanjan

DSP	: Digital signal processing	: Mr. K. Gurucharan
MW	: Microwave Engineering	: Ms.B.Sarada
ITC	: Information theory and coding	: Mr. S. S. Kiran
VLSI	: VLSI and Embedded systems	: Mr.ManasRanjan
TV&SAT	: TV and Satellite communications	: Ms.Rupalini Nanda
DSP Lab	: Digital signal processing Lab	: Mr. K. Gurucharan
DC	: Digital communication lab	: Mr. S.V Anand

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## M.Tech (CS) TIME TABLE

Branch: M.Tech (CS)		Semester: I		Room. No: S3		w. e. f:09/10/2017		
DAY	9:30-10:20	10:20- 11:10	11:10-12:00	12:00-1:00	1:00PM TO 1:40PM LUNCH	1:40-2:30	2:30-3:20	3:20-4:10
MON	OFA	DSP	EMC	SCPA		COTH		COMT
TUE	SCPA	COMT	OFA	EMC		COTH	DSP	
WED	EMC	SCPA	COMT			SEMINAR		
THU	DSP	EMC		COTH		OFA		SCPA
FRI	DSP	OFA	COTH	SCPA		CE LAB		
SAT	COTH	DSP	EMC	OFA		SCPA	COMT	

CLASS IN-CHARGE: Dr C Ghanshyam

COMT Communication Theory  
 COTH Communication Techniques  
 SCPA Satellite Communication and Phased Arrays  
 DSP Digital signal processing  
 OFA Optical Fibers and Applications  
 EMC Elective –I EMI/EMC  
 CE LAB Communication Engineering Lab  
 Seminar - I

Mr. A. Satyanarayana  
 Mr. Dr C Ghanshyam  
 Mrs.Seema B Thakur  
 Mr. R Ravindra Prasad  
 G B Pavan Kumar  
 Mr. K. Suresh Kumar  
 Ms.B.Sarada  
 Dr C Ghanshyam

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## M.Tech (VLSI) TIME TABLE

Branch: M.Tech (VLSI) Semester: I Room. No: S4 w. e. f:09/10/2017								
DAY	9:30-10:20	10:20- 11:10	11:10-12:00	12:00-1:00	1:00PM	1:40-2:30	2:30-3:20	3:20-4:10
MON	VLSI	DSP	AICD	AMM	TO	DSD		EMC
TUE	AMM	EMC	VLSI	MW		DSD	DSP	
WED	AICD	AMM	EMC		1:40PM	SEMINAR		
THU	DSP	AICD		DSD	LUNCH	VLSI		AMM
FRI	DSP	VLSI	DSD	AMM		HDL LAB		
SAT	DSD	DSP	AICD	VLSI		AMM	EMC	

CLASS IN-CHARGE: Mr. A Rama Krishna

VLSI VLSI Design Techniques  
 AMM Advanced Microprocessors and Microcontrollers  
 AICD Analog IC design  
 DSD Digital System Design  
 DSP Elective – I a) Digital Signal Processing  
 EMC Elective – II a) EMI/EMC  
 HDL LAB HDL Programming Laboratory  
 Seminar-I

Mr. A Rama Krishna  
 Dr. Nihar Ranjan Panda  
 Mr. B. Sangeeth Kumar  
 Mr. T. Ravi Babu  
 Mr. R Ravindra Prasad  
 Mr. K. Suresh Kumar  
 Dr.Nihar Ranjan Panda  
 Mr. A Rama Krishna

CLASS IN-CHARGE

HOD-ECE